

a power supply input detection unit determining whether an input of said second power supply input terminal is above a predetermined value;

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a switching unit cutting off power supplied by said first power supply input terminal via the interface, and activating a power supplied by said second power supply input terminal via the AC adaptor if the input to said second power supply input terminal is above the predetermined value even if the input to said second power supply input terminal via the interface is present; and

a power supply processor processing the power supplied by one of said first or second power supply input terminals.

28 (ONCE AMENDED) The power supply control device according to claim 1, wherein said switching unit comprises:

B2 a switch activating or deactivating the power supplied by said first power supply input terminal; and

a switch control unit driving the switch thereby cutting off the power supplied by said first power supply input terminal if the power supplied by said second terminal is greater than the predetermined value.

45 (ONCE AMENDED) The power supply control device according to claim 1, wherein said switching unit comprises:

B3 *Cont.* a first switch activating or deactivating the power supplied by said first power supply input terminal;

a second switch which activating or deactivating the power supplied by said second power supply input terminal; and

a switch control unit driving one of the first and second switches, thereby activating the power supplied by said first power supply input terminal when there is no power supplied by said second power supply input terminal according to a result from said power supply input detection unit, and driving the other of the first and second switches, thereby activating the power supplied by said second power supply input terminal when the power supplied by said second power supply input terminal is greater than the predetermined value.

56 (ONCE AMENDED) The power supply control device according to claim *2*, wherein the switch comprises a field effect transistor.

4. (ONCE AMENDED) An information processing device having an interface which receives or transmits information to and from another information processing device and a power supply control device to which a predetermined power supply is provided, comprising:

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a first power supply input terminal to which a power supply is provided via said interface;
a second power supply input terminal to which a power supply is provided via an AC adaptor;

a power supply input detection unit which detects an instance when the input of said second power supply input terminal via the AC adaptor is above a predetermined value;

a switch unit which cuts off a power supply from said first power supply input terminal via the interface and activates a power supply from said second power supply input terminal via the AC adaptor when the input from said second power supply input terminal is above the predetermined value according to said power supply input detection unit even if the input to said second power supply input terminal via the interface is present; and

a power supply processor which, for the predetermined power supply, processes the power supply supplied via said first or second power supply input terminals.

34 *10.* (ONCE AMENDED) The information processing device according to claim *7*,
wherein said switch unit comprises:

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a switch which activates or deactivates a power supply from said first power supply input terminal; and

a switch control unit which drives the switch and thereby cuts off a power supply from said first power supply input terminal when there is a supply of more than the predetermined value from said second power supply input terminal according to a result from said power supply input detection unit.

9 *10.* (ONCE AMENDED) The information processing device according to claim *9*,
wherein a backflow prevention unit is disposed in a power supply lead from said first power supply input terminal and said second power supply input terminal, respectively.

9 *11.* (TWICE AMENDED) The information processing device according to claim *7*,
wherein said switch unit comprises:

a first switch which activates or deactivates the power supply from said first power supply input terminal;

a second switch which activates or deactivates the power supply from said second power supply input terminal; and

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and*
a switch control unit which drives one switch and thereby activates the power supply from said first power supply input terminal when there is no power supply from said second power supply input terminal according to a result from said power supply input detection unit, and which drives another switch and thereby activates the power supply from said second power supply input terminal when the power supplied by said second power supply input terminal is greater than the predetermined value.

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13.15. (ONCE AMENDED) A power supply control device comprising:

a first power supply input terminal to which a power supply is provided via an interface;
a second power supply input terminal to which a power supply is provided via an AC adaptor;

a detection unit determining whether an input of said second power supply input terminal is above a predetermined value; and

a switching unit cutting off power supplied by said first input terminal via the interface and activating a power supplied by said second input terminal via the AC adaptor if the input to said second input terminal is above the predetermined value even if the input to said second power supply input terminal via the interface is present.

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16. (ONCE AMENDED) A method of controlling a power supply control device comprising:

providing a power supply to first and second power supply input terminals via an interface and AC adaptor, respectively;

determining whether an input of the second power supply input terminal via the AC adaptor is above a predetermined value; and

switching off power supplied by the first input terminal via the interface and activating a power supplied by the second input terminal via the AC adaptor if the input to the second input terminal is above the predetermined value even if the input to said second power supply input terminal via the interface is present.